

AMENDMENTS TO THE SPECIFICATION:

A substitute Abstract of the Disclosure is provided on an accompanying separate sheet provided in the Appendix.

Amend the title throughout, declaration excepted, as follows:

--SYSTEM OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT BY  
HAVING A CLIENT CONNECTED TO A MANUFACTURER VIA TWO-WAY  
COMMUNICATION--

Page 2, replace the paragraph, beginning on line 4, with the following amended paragraph:

--Recently, attempts have been made to use the Internet, which has been widespread rapidly in these years, for the production of ASICs. For example, according to a system proposed by Quicklogic Corp., a client releases layout data from an FPGA developing tool (FPGA: field programmable gate array) via the Internet ~~(see <http://www.quicklogic.com/webasic>)~~. A manufacturing center writes the received data into FPGA chips, and sends completed circuit chips to the client via a normal transporting means. For another example, according to a system proposed by Clear Logic Inc., a client creates layout data for FPGAs of a different company (Altera Corp.), and sends the data via the Internet ~~(see <http://www.clear-logic.com/pressrelease/11-29/99.htm>)~~. A manufacturing center uses the received data, thereby manufactures products of its own, and sends completed

products (circuit chips) to the client by means of a normal  
transporting means.--